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RFC 3215

Title: LDP State Machine
Author(s): C. Boscher, P. Cheval, L. Wu, E. Gray
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Date: January 2002
Mailbox: christophe.boscher@alcatel.fr, pierrick.cheval@space.alcatel.fr, liwwu@cisco.com, eric.gray@sandburst.com
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This document provides state machine tables for ATM (Asynchronous Transfer Mode) switch LSRs. In the current LDP specification, there is no state machine specified for processing LDP messages. We think that defining a common state machine is very important for interoperability between different LDP and CR-LDP implementations.

We begin in section 1 by defining a list of terminologies. Then in section 2, we propose two sets of state machine tables for ATM switch LSRs that use downstream-on-demand mode, one method can be used for non-vc merge capable ATM LSRs, while the other one can be used for the vc-merge capable ATM LSRs. In section 3, we provides a state machine for downstream unsolicited mode ATM LSRs.

We focus on the LDP state machines and the associated control blocks used for establishing and maintaining LSPs. We do not describe state machines for the "LDP controller" that is in charge of LDP session initialization, address mapping messages management, routing interface, etc. that is defined in the LDP specification.

Even though the state machines in this document are specific for ATM-LSR, they can be easily adapted for other types of LSRs.

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